

ABSTRACT

An electronic device, such as a microprocessor , with a timing circuit. The timing circuit contains a phase locked loop that, during a first interval, checks whether a control signal
5 in the phase locked loop is between a maximum allowed value and a minimum allowed value. When the control signal in the phase locked loop is above a maximum allowed value or below a minimum allowed value, the control circuit disables the phase locked loop for a second interval. When the control signal in the phase locked loop is below a maximum allowed value and above a minimum allowed value, the timing circuit
10 indicates that the output of the phase locked loop is stable.